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**TEXAS INSTRUMENTS INCORPORATED
P O BOX 655474, M/S 3999
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05/05/2005 TBESHAH2 00000028 200666 09875136

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Robin E. Barnum (Depositor's name)

(Signature)

May 3, 2005 (Date)

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 09/875,136 | 06/06/2001 | Robert J. Gove | TI-14657A.2 | 8645 |

TITLE OF INVENTION: SINGLE INTEGRATED CIRCUIT EMBODYING A DUAL HETEROGENOUS PROCESSORS WITH SEPARATE INSTRUCTION HANDLING HARDWARE

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|----------------|--------------|-----------|-----------------|------------------|------------|
| nonprovisional | NO | \$1400 | \$0 | \$1400 | 07/20/2005 |

| EXAMINER | ART UNIT | CLASS-SUBCLASS |
|------------------|----------|----------------|
| TREAT, WILLIAM M | 2183 | 712-035000 |

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2. For printing on the patent front page, list

(1) the names of up to 3 registered patent attorneys or agents OR, alternatively,

(2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 Robert D. Marshall, Jr.2 W. James Brady, III3 Frederick J. Telecky, Jr.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Texas Instruments Incorporated

(B) RESIDENCE: (CITY AND STATE OR COUNTRY)

Dallas, TX USA

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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5. Change in Entity Status (from status indicated above)

☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

The Director of the USPTO is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above. NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

Authorized Signature

Robert D. Marshall, Jr.Date May 3, 2005

Typed or printed name

Robert D. Marshall, Jr.

Registration No. 28,527

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Robert J. Gove, et al.

Serial No.: 09/875,136

Filed: June 6, 2001

For: Single Integrated Circuit Embodying a Dual Heterogenous Processors With Separate Instruction Handling Hardware

TI-14657A.2

Art Unit: 2183

Examiner: William M. Treat

Conf. No.: 8645

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-746-4000 on the date shown below:

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FACSIMILE COVER SHEET

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| NAME OF INVENTOR(S): Robert J. Gove, et al. | |
| TITLE OF INVENTION: Single Integrated Circuit Embodying a Dual Heterogenous Processors With Separate Instruction Handling Hardware | |
| TI FILE NO.: TI-14657A.2 | DEPOSIT ACCT. NO.: 20-0668 |
| RECEIPT DATE & SERIAL NO.: Serial No.: 09/875,136 Filing Date: June 6, 2001 | |
| FAXED: 5/3/05 DUE: 7/20/05 ATTY/SECY: RDM/reb | |

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